

APPROVED: /AN/

ANNOTATED SHEET

U.S. Patent Application No. 10/787,159

Attorney Docket No. 1740-000088/US

6/8

FIG. 6

← 1 byte →		← 4 bytes →					
SB(3,3)	BCA pre-amble (all 00h)					1 row	
SB(0,0)	I(0,0)	I(1,0)	I(2,0)	I(3,0)	4 rows data	1st data unit	
SB(0,0)	I(4,0)	I(5,0)	I(6,0)	I(7,0)			
SB(0,0)	I(8,0)	I(9,0)	I(10,0)	I(11,0)			
SB(0,0)	I(12,0)	I(13,0)	I(14,0)	I(15,0)			
SB(0,1)	C(0,0)	C(1,0)	C(2,0)	C(3,0)	4 rows parity		
SB(0,1)	C(4,0)	C(5,0)	C(6,0)	C(7,0)			
SB(0,1)	C(8,0)	C(9,0)	C(10,0)	C(11,0)			
SB(0,1)	C(12,0)	C(13,0)	C(14,0)	C(15,0)			
SB(0,2)	I(0,1)	I(1,1)	I(2,1)	I(3,1)	4 rows data	2nd data unit	
SB(0,2)	I(4,1)	I(5,1)	I(6,1)	I(7,1)			
SB(0,2)	I(8,1)	I(9,1)	I(10,1)	I(11,1)			
SB(0,2)	I(12,1)	I(13,1)	I(14,1)	I(15,1)			
SB(0,3)	C(0,1)	C(1,1)	C(2,1)	C(3,1)	4 rows parity		
SB(0,3)	C(4,1)	C(5,1)	C(6,1)	C(7,1)			
SB(0,3)	C(8,1)	C(9,1)	C(10,1)	C(11,1)			
SB(0,3)	C(12,1)	C(13,1)	C(14,1)	C(15,1)			
SB(1,0)	I(0,2)	I(1,2)	I(2,2)	I(3,2)	4 rows data	3rd data unit	
SB(1,0)	I(4,2)	I(5,2)	I(6,2)	I(7,2)			
SB(1,0)	I(8,2)	I(9,2)	I(10,2)	I(11,2)			
SB(1,0)	I(12,2)	I(13,2)	I(14,2)	I(15,2)			
SB(1,1)	C(0,2)	C(1,2)	C(2,2)	C(3,2)	4 rows parity		
SB(1,1)	C(4,2)	C(5,2)	C(6,2)	C(7,2)			
SB(1,1)	C(8,2)	C(9,2)	C(10,2)	C(11,2)			
SB(1,1)	C(12,2)	C(13,2)	C(14,2)	C(15,2)			
SB(1,2)	I(0,3)	I(1,3)	I(2,3)	I(3,3)	4 rows data	4th data unit	
SB(1,2)	I(4,3)	I(5,3)	I(6,3)	I(7,3)			
SB(1,2)	I(8,3)	I(9,3)	I(10,3)	I(11,3)			
SB(1,2)	I(12,3)	I(13,3)	I(14,3)	I(15,3)			
SB(1,3)	C(0,3)	C(1,3)	C(2,3)	C(3,3)	4 rows parity		
SB(1,3)	C(4,3)	C(5,3)	C(6,3)	C(7,3)			
SB(1,3)	C(8,3)	C(9,3)	C(10,3)	C(11,3)			
SB(1,3)	C(12,3)	C(13,3)	C(14,3)	C(15,3)			
SB(3,2)							

b7	b6	b5	b4	b3	b2	b1	b0
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00	: 23G -- 80nm
01	: 25G -- 75.4nm 74.5nm
10	: 27G -- 69nm
11	: reserved